TRISTATE STRUCTURES FOR PROGRAMMABLE LOGIC DEVICES

ABSTRACT OF THE DISCLOSURE

A programmable logic device architecture including tristate structures. The programmable logic device architecture provides tristate structures which may be logically or programmably controlled, or both. Through these tristate structures, the logic elements may be coupled to the programmable interconnect, where they may be coupled with other logic elements of the programmable logic device. Using these tristate structures, the signal pathways of the architecture may be dynamically reconfigured.

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